

## **LISTING OF THE CLAIMS**

This listing of claims will replace all prior versions, and listings, of claims in the application:

### **Claim 1 (original)**

A control circuit for controlling a hysteretic regulator to operate at substantially fixed frequency, comprising:

a voltage offset circuit which receives a switching frequency feedback signal and an output voltage feedback signal from said hysteretic regulator and generates a pair of upper and lower offset voltages;

a control reference stage which receives a reference voltage and said upper and lower offset voltages and outputs upper and lower control reference voltages; and  
a window comparator which receives said upper and lower control reference voltages and said output voltage feedback signal and outputs a pair of upper and lower control voltages for controlling switching of said hysteretic regulator at a fixed frequency.

### **Claim 2 (original)**

A control circuit as set forth in claim 1, wherein said control reference stage comprises a first summer which receives said reference voltage and said upper offset voltage, and a second summer which receives said reference voltage and said lower offset voltage; and said first and second summers respectively output said upper and lower control reference voltages.

### **Claim 3 (original)**

A control circuit as set forth in claim 2, wherein said first summer adds said upper offset voltage to said reference voltage, and said second summer subtracts said lower offset voltage from said reference voltage.

**Claim 4 (original)**

A control circuit as set forth in claim 1, wherein said voltage offset circuit comprises a frequency/voltage converter which receives said frequency feedback signal.

**Claim 5 (original)**

A control circuit as set forth in claim 4, wherein said voltage offset circuit further comprises an error amplifier in series with said frequency/voltage converter.

**Claim 6 (original)**

A control circuit as set forth in claim 5, further comprising an output filter in series with said error amplifier.

**Claim 7 (original)**

A control circuit as set forth in claim 4, further comprising an output filter in series with said frequency/voltage converter.

**Claim 8 (original)**

A control circuit as set forth in claim 4, further comprising a resistive network connected to said voltage offset circuit for delivering said upper and lower offset voltages to said control reference stage.

**Claim 9 (original)**

A control circuit as set forth in claim 8, wherein said resistive network further delivers said reference voltage to said control reference stage.

**Claim 10 (original)**

A control circuit as claimed in claim 1, wherein said upper and lower control voltages are positioned symmetrically about a nominal voltage.

**Claim 11 (original)**

A control circuit as claimed in claim 1, wherein said upper and lower control voltages are positioned asymmetrically about a nominal voltage.

**Claim 12 (original)**

A control circuit as claimed in claim 1, wherein said upper control voltage is positioned higher than said lower control voltage.

**Claim 13 (original)**

A control circuit as claimed in claim 12, wherein said upper and lower control voltages are positioned asymmetrically about a nominal voltage.

**Claim 14 (original)**

A control circuit as claimed in claim 1, wherein said upper control voltage is positioned lower than said lower control voltage.

**Claim 15 (original)**

A control circuit as claimed in claim 14, wherein said upper and lower control voltages are positioned asymmetrically about a nominal voltage.

**Claim 16 (original)**

A control circuit as claimed in claim 1, wherein at least one of said upper and lower control voltages is positioned above a nominal voltage.

**Claim 17 (original)**

A control circuit as claimed in claim 26, wherein both of said upper and lower control voltages are positioned above said nominal voltage.

**Claim 18 (original)**

A control circuit as claimed in claim 1, wherein both of said upper and lower control voltages are positioned below said nominal voltage.

**Claim 19 (original)**

A control circuit as claimed in claim 1, wherein said upper and lower control voltages are substantially equal.

**Claim 20 (original)**

A control circuit for controlling a hysteretic regulator to operate at substantially fixed frequency, comprising:

a voltage offset circuit which receives a first feedback signal and generates a pair of upper and lower offset voltages;

a control reference stage which receives a reference voltage and said upper and lower offset voltages and outputs upper and lower control reference voltages; and

a window comparator which receives said upper and lower control reference voltages and an output voltage feedback signal and outputs a pair of upper and lower control voltages for controlling switching of said hysteretic regulator at a fixed frequency.

**Claim 21 (original)**

A control circuit as claimed in claim 20, wherein said first feedback signal is representative of a switching frequency of said hysteretic regulator.

**Claim 22 (original)**

A control circuit as claimed in claim 20, wherein said first feedback signal is representative of a temperature of said hysteretic regulator.

**Claim 23 (original)**

A control circuit as claimed in claim 20, wherein said voltage offset circuit further receives a second feedback signal.

**Claim 24 (original)**

A control circuit as claimed in claim 23, wherein said first and second feedback signals are representative of a switching frequency and a temperature of said hysteretic regulator, respectively.

**Claim 25 (original)**

A control circuit as claimed in claim 23, wherein said voltage offset circuit further comprises a circuit for weighting said first and second feedback signals.

**Claim 26 (original)**

A control circuit as claimed in claim 23, wherein said second feedback signal is said output voltage feedback signal.

**Claim 27 (original)**

A control circuit as claimed in claim 26, wherein said voltage offset circuit further comprises a circuit for weighting said first and second feedback signals.

**Claim 28 (original)**

A control circuit as claimed in claim 23, wherein said voltage offset circuit further receives at least one additional feedback signal.

**Claim 29 (original)**

A control circuit as claimed in claim 28, wherein said voltage offset circuit further comprises a circuit for weighting said first, said second, and said additional feedback signals.

**Claim 30 (New)**

A control circuit as claimed in claim 10, wherein said upper and lower control voltages are substantially equal.

**AMENDMENT TO THE DRAWING(S)**

The attached sheets of formal drawings replaces the original sheets including Figs. 1-10.